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As discussed in the above question, the V_t depends on the voltage connected to the Body terminal. It also depends on the temperature, the magnitude of V_t decreases by about 2mV for every 1oC rise in temperature. 10. Give the Cross-sectional diagram of the CMOS. VLSI VIVA Questions and Answers :

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1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

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VLSI Lab viva question with answers1 0 X11 1 X0D flip-flopQ(t) Q(t+1) D0 0 00 1 11 0 01 1 1T flip-flopQ(t) Q(t+1) T0 0 00 1 11 0 11 1 059. Design a BCD counter with JK flip-flopsAnswer60. Design a counter with the following binary sequence 0, 1, 9, 3, 2, 8, 4 and ...

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VLSI LAB VIVA Questions MOSFET has three regions of operations. 1.Cut-off region 2.Triode region 3.Saturation region. The triode and cut-off region are used to function as a switch, while, saturation region is used to operate as an amplifier. Posts navigation

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Viva Question5 and Answer5 in MP InterfacinG LaB - 1 1) What is MicroProcessor ? A:- A MicroProcessor is a multipurpose, programmable, clock-driven, register-based electronic device that reads binary instructions from a storage device called memory , accepts binary data as input and processes the data according to those instruction, and provides results as output.

<h3>Viva Questions and Answers in MP <i>/i> lab -1</h3>

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A DLL in its simplest form inserts a variable delay line between the external clock and the internal clock. The clock tree distributes the clock to all registers and then back to the feedback pin of the DLL. The control circuit of the DLL adjusts the delays so that the rising edges of the feedback clock align with the input clock.

Core VLSI: FPGA Interview Questions

To be precise about Very-large-scale integration is the procedure of creating a combined circuit by merging hundreds of thousands of transistors or devices into a single chip. If you are looking at VLSI with VLSI Engineer, Embedded & VLSI Engineers, VLSI Developer, Formal Verification then we've framed multiple VLSI interview questions and answers and also various VLSI job roles also for ...

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Microprocessor Lab Vtu Viva Questions With Answer

DSP stands for Digital Signal Processing. DSP is a very important subject for Engineering and Diploma students. It is basically a numerical paper but it also consists of some very important theory portions that are required to be studied well as beginners.